

MOSFETs Silicon N-channel MOS (U-MOSVII-H)

TK160F10N1

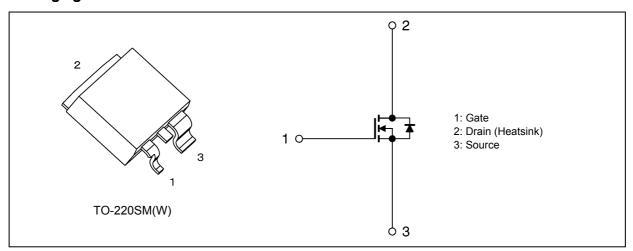
1. Applications

- · Automotive
- · Switching Voltage Regulators
- · DC-DC Converters
- · Motor Drivers

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)}$ = 2.0 m Ω (typ.) (V_{GS} = 10 V)
- (2) Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 100 \text{ V)}$
- (3) Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V _{DSS}	100	V
Gate-source voltage			V_{GSS}	±20	
Drain current (DC)		(Note 1)	I _D	160	Α
Drain current (pulsed)		(Note 1)	I _{DP}	480	
Power dissipation	(T _c = 25 °C)	(Note 2)	P _D	375	W
Single-pulse avalanche energy		(Note 3)	E _{AS}	932	mJ
Single-pulse avalanche current			I _{AS}	80	Α
Channel temperature		(Note 4)	T _{ch}	175	°C
Storage temperature		(Note 4)	T _{stg}	-55 to 175	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

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Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	0.4	°C/W

- Note 1: Ensure that the channel temperature does not exceed 175 °C.
- Note 2: The power dissipation value is calculated based on the channel-to-case thermal resistance. However, the safe operating area is not only limited to thermal limits but also the current concentration phenomenon. This device should not be used under conditions outside its safe operating area shown herein.
- Note 3: V_{DD} = 80 V, T_{ch} = 25 °C (initial), L = 112 μ H, R_G = 1 Ω , I_{AS} = 80 A, V_{GS} = +15/-10 V
- Note 4: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



6. Electrical Characteristics

6.1. Static Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Drain-source breakdown voltage	V _{(BR)DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	65		_	
Gate threshold voltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 80 A		2.0	2.4	mΩ

6.2. Dynamic Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 300 kHz	_	8510		pF
Reverse transfer capacitance	C _{rss}		_	500		
Output capacitance	C _{oss}		_	3960	_	
Switching time (rise time)	t _r	See Fig. 6.2.1.	_	22	_	ns
Switching time (turn-on time)	t _{on}		_	54	_	ns
Switching time (fall time)	t _f		_	46	_	
Switching time (turn-off time)	t _{off}		_	127		

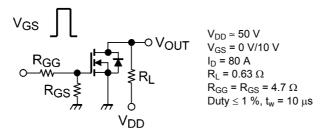


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 160 \text{ A}$	_	121	_	nC
Gate-source charge 1	Q _{gs1}		_	40	_	nC
Gate-drain charge	Q_{gd}		_	28	_	nC

6.4. Source-Drain Characteristics (T_a = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I _{DR}	_	_	_	160	Α
Reverse drain current (pulsed)	(Note 5)	I _{DRP}	_	_	_	480	
Diode forward voltage		V_{DSF}	I _{DR} = 160 A, V _{GS} = 0 V	_	_	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 160 A, V _{GS} = 0 V	_	107	_	ns
Reverse recovery charge		Q _{rr}	V _{DD} = 80 V -dl _{DR} /dt = 50 A/μs	_	144		nC

Note 5: Ensure that the channel temperature does not exceed 175 °C.



7. Marking (Note)

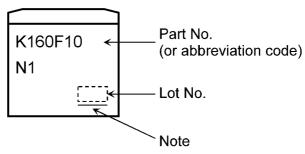


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



8. Moisture-Proof Packing

This device is packed in a moisture-proof laminated aluminum bag.

8.1. Precautions for Transportation and Storage (Note)

- (1) Avoid excessive vibration during transportation.
- (2) Do not toss or drop the packed devices to avoid ripping of the bag.
- (3) After opening the moisture-proof bag, the devices should be assembled within two weeks in an environment of 5 °C to 30 °C and RH70 % or below. Perform reflow at most twice.
- (4) The moisture-proof bag may be stored unopened for up to 24 months at 5 °C to 30 °C and RH90 % or below.
- (5) If, upon opening the bag, the moisture indicator card shows humidity of 30 % or above (the color of the 30 % dot has changed from blue to pink) or the expiration date has passed, the devices should be baked as follows:

Baking conditions: 125 °C for 48 hours.

Note: Since the tape materials are not heat-proof, devices should be placed on either heat-proof trays or aluminum magazines when baking.



Fig. 8.1.1 Humidity Indicator

The humidity indicator shows an approximate ambient humidity at 25 $^{\circ}$ C. If the ambient humidity is below 30 %, the color of all the indicator dots is blue. If, upon opening the bag, the color of the 30 % dot has changed from blue to pink, the devices should be baked before assembly.

9. Characteristics Curves (Note)

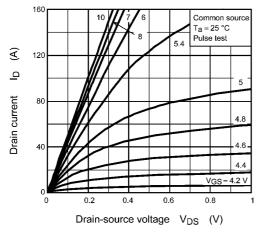
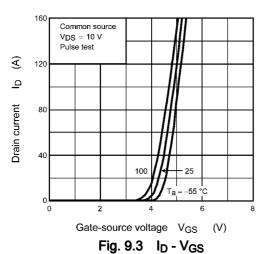


Fig. 9.1 I_D - V_{DS}



Prain-source on-resistance

RDS(ON) (mΩ)

A DE = SON

Common source

T_a = 25 °C Pulse test

Drain current I_D (A) Fig. 9.5 R_{DS(ON)} - I_D

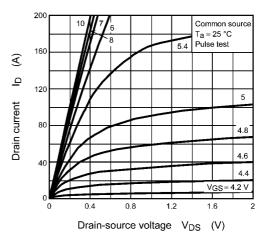


Fig. 9.2 I_D - V_{DS}

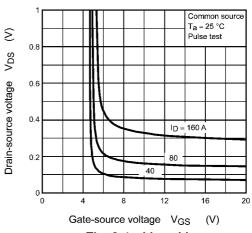


Fig. 9.4 V_{DS} - V_{GS}

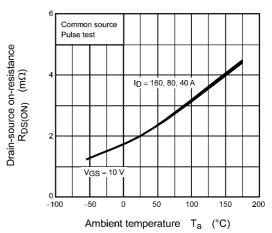


Fig. 9.6 R_{DS(ON)} - T_a

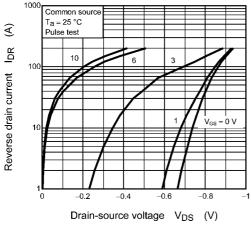


Fig. 9.7 IDR - VDS

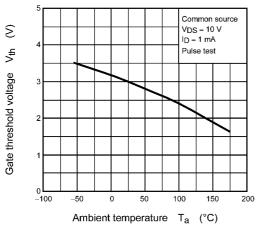
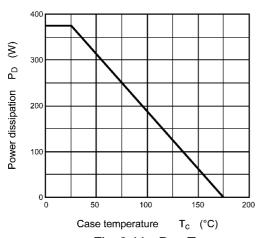


Fig. 9.9 V_{th} - T_a



 $\label{eq:Fig. 9.11 PD - Tc} \textbf{(Guaranteed Maximum)}$

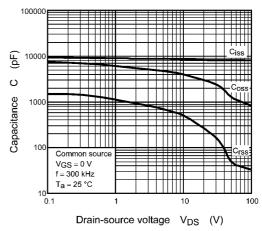


Fig. 9.8 Capacitance - V_{DS}

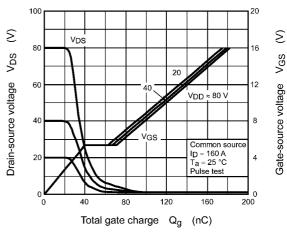


Fig. 9.10 Dynamic Input/Output Characteristics

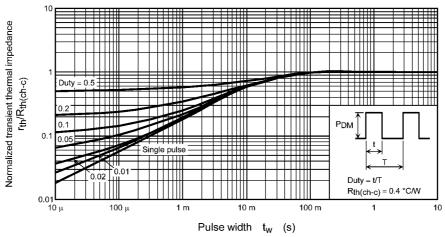


Fig. 9.12 $r_{th}/R_{th(ch-c)} - t_w$ (Guaranteed Maximum)

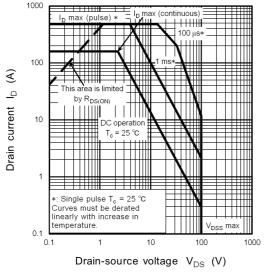


Fig. 9.13 Safe Operating Area (Guaranteed Maximum)

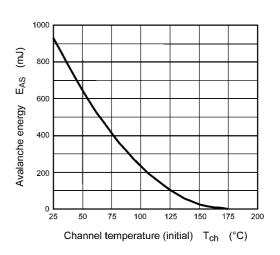


Fig. 9.14 E_{AS} - T_{ch} (Guaranteed Maximum)

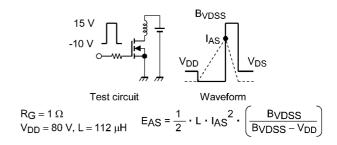


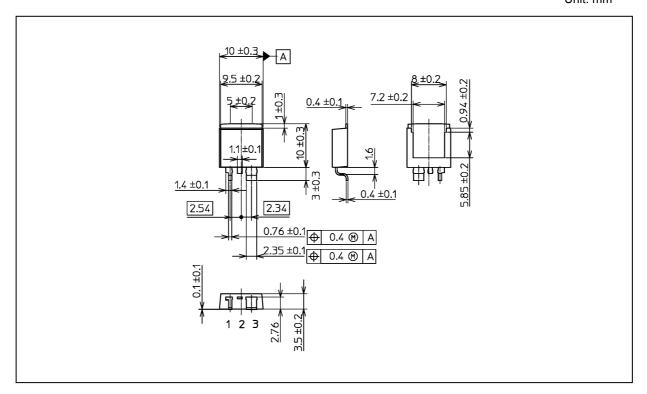
Fig. 9.15 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 1.07 g (typ.)

Package Name(s)			
TOSHIBA: 2-10W1S			
Nickname: TO-220SM(W)			



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