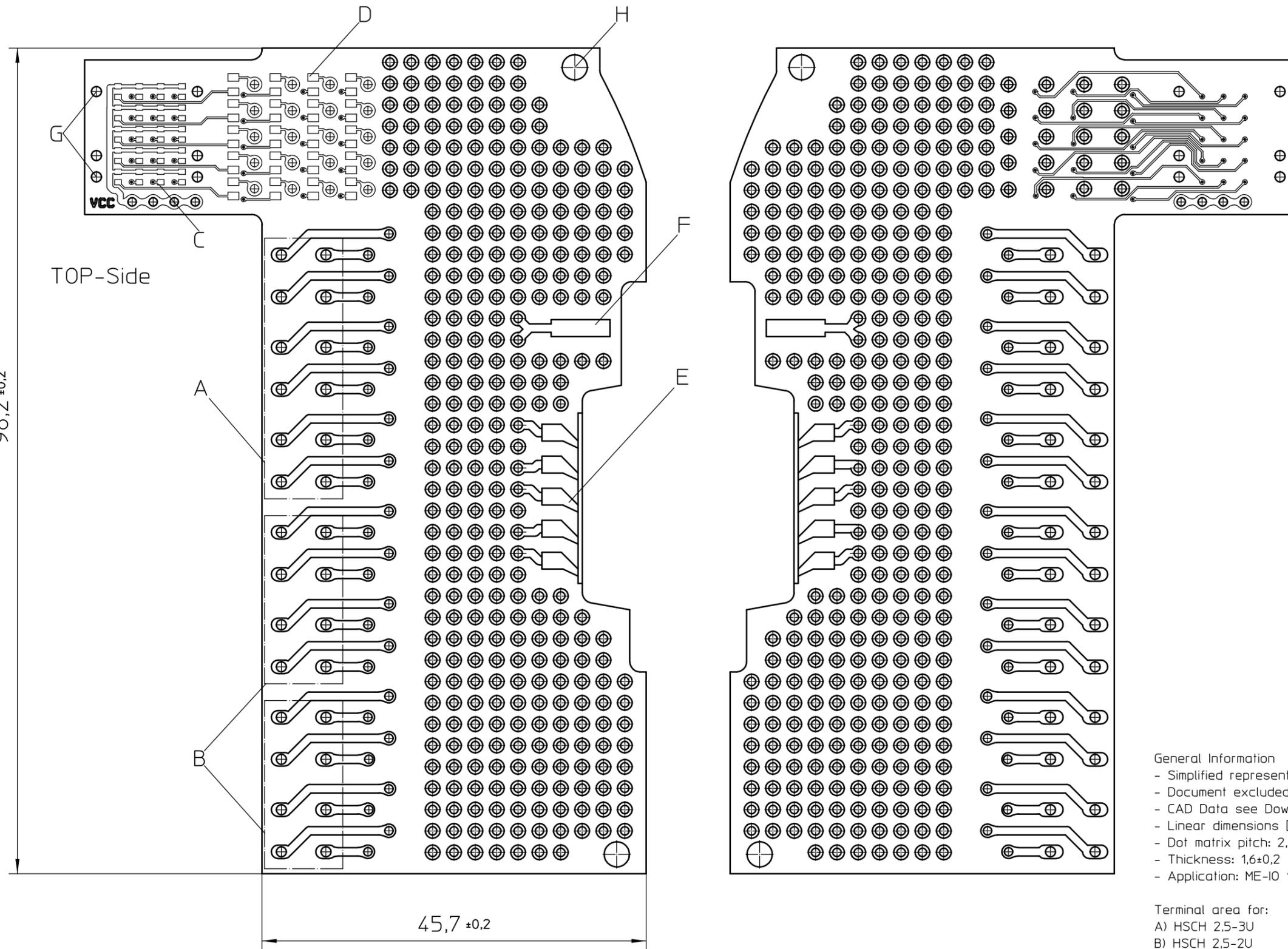


Transfer or duplication of this document as well as the utilisation or communication of its content are not permitted unless express consent is granted. Violations give rise to claims for damages. All rights reserved in case of patent, utility model or design patent registrations.

Weitergabe sowie Vervielfältigung dieses Dokuments, Verwertung und Mitteilung seines Inhalts sind verboten, soweit nicht ausdrücklich gestattet. Zuwiderhandlungen verpflichten zu Schadenersatz. Alle Rechte für den Fall der Patent-, Gebrauchsmuster- oder Designeintragung vorbehalten.

98,2 ±0,2

TOP-Side



45,7 ±0,2

- General Information
- Simplified representation
 - Document excluded from change process
 - CAD Data see Download-Center on www.phoenixcontact.com
 - Linear dimensions [mm]
 - Dot matrix pitch: 2,54mm ; drill size: $\varnothing 1$
 - Thickness: 1,6±0,2
 - Application: ME-IO 18,8 DEV-KIT

- Terminal area for:
- A) HSCH 2,5-3U
 - B) HSCH 2,5-2U
 - C) Pad for SMD LED 0603
 - D) Pad for SMD Resistor 0603
 - E) Pad for TBUS CONTACT, surface Au
 - F) Pad for FE CONTACT, surface Sn
 - G) Mounting hole for HS LC-H-D2/ R4XC5-2,54
 - H) Hole for latching with ME-IO 18,8 B

	document-No. / RI	date	scale
	00864564 / 00	27.04.2015	2:1
	ident.-No.	document-type	
	2202548	PCB outline	
page 1	of 1	description	
ME-IO 18,8/28 TBUS DEV-PCB			